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In re Application of:

Keisuke GOTO, et al.

Appl. No.: 09/826,179

Filed: April 4, 2001

For: DLL CIRCUIT AND METHOD GENERATING

**TIMING SIGNALS** 

Art Unit: 2816

Examiner: LE, Dinh Thanh

Atty Docket: KPM-01501

## Certificate of Mailing

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as first class mail, postage prepaid, "Post Office to Addressee", in an envelope addressed to: Commissioner for Patents, Washington, DC 20231, on March 13, 2003.

## PETITION FOR EXTENSION OF TIME

Commissioner for Patents Washington, DC 20231

Sir:

This is a request to extend the period for filing a response in the above-identified application for three months from December 16, 2002 to March 16, 2003. A check for \$930.00 to cover the extension fee is enclosed. Although we believe that we have submitted the correct amount to cover the requested extension of time, the Commissioner is authorized to credit any overpayment or charge any deficiencies to our **Deposit Account No. 031721**. Two originally-executed copies of this form are being submitted.

Should there be any questions after reviewing this paper, the Examiner is invited to contact the undersigned at (617) 248-4038.

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Date: March 13, 2003

Choate, Hall & Stewart Patent Group Exchange Place 53 State Street Boston, MA 02109-2804 Respectfully submitted,

Donald W. Muirhead Reg. No. 33,978